



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Robert B. Staszewski, et al.

Serial No.: **09/695,516**

Filed: **10/24/2000**

For: **HYBRID OF PREDICTIVE AND CLOSED-LOOP PHASE DOMAIN
DIGITAL PLL ARCHITECTURE**

Docket No.: **TI-30674**

Examiner: **Kinhead**

Art Unit: **2817**

Confirm. No.: **311**

SEP 25 2002

RECEIVED

EXTENSION OF TIME

September 17, 2002

Assistant Commissioner For Patents

Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that on this date the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.

Elizabeth Austin

Date

9/17/2002

Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicants respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

<input checked="" type="checkbox"/> One Month	(\$110)	<input type="checkbox"/> Four Months	(\$1,440)
<input type="checkbox"/> Two Months	(\$400)	<input type="checkbox"/> Five Months	(\$1,960)
<input type="checkbox"/> Three Months	(\$920)		

Any further necessary extension of time is hereby requested. Charge any and all fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. An original and two copies of this sheet are enclosed.

Respectfully submitted.

Texas Instruments Incorporated
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Registration No. 34,227

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